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Relevance scale ☐ ☐ ☐ ☐ ☐**1 [Solving thematic divergences in machine translation](#)**

Bonnie Dorr

June 1990 **Proceedings of the 28th conference on Association for Computational Linguistics**Full text available: [pdf\(476.05 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#) [Publisher Site](#)

Though most translation systems have some mechanism for translating certain types of divergent predicate-argument structures, they do not provide a general procedure that takes advantage of the relationship between lexical-semantic structure and syntactic structure. A divergent predicate-argument structure is one in which the predicate (e.g., the main verb) or its arguments (e.g., the subject and object) do not have the same syntactic ordering properties for both the source and tar ...

**2 [LLVA: A Low-level Virtual Instruction Set Architecture](#)**

Vikram Adve, Chris Lattner, Michael Brukman, Anand Shukla, Brian Gaeke

December 2003 **Proceedings of the 36th Annual IEEE/ACM International Symposium on Microarchitecture**Full text available: [pdf\(196.08 KB\)](#)Additional Information: [full citation](#), [abstract](#), [index terms](#) [Publisher Site](#)

A virtual instruction set architecture (V-ISA) implemented via a processor-specific software translation layer can provide great flexibility to processor designers. Recent examples such as Crusoe and DAISY, however, have used existing hardware instruction sets as virtual ISAs, which complicates translation and optimization. In fact, there has been little research on specific designs for a virtual ISA for processors. This paper proposes a novel virtual ISA (LLVA) and a translation strategy for implementi ...

**3 [Translator writing systems](#)**

Jerome Feldman, David Gries

February 1968 **Communications of the ACM**, Volume 11 Issue 2Full text available: [pdf\(4.47 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)


A critical review of recent efforts to automate the writing of translators of programming languages is presented. The formal study of syntax and its application to translator writing are discussed in Section II. Various approaches to automating the postsyntactic (semantic) aspects of translator writing are discussed in Section III, and several related topics in Section IV.

**Keywords:** compiler compiler-compiler, generator, macroprocessor, meta-assembler, metacompiler, parser, semantics, syntactic analysis, syntax, syntax-directed, translator, translator writing system

4 A simply extended and modified batch environment graphical system (SEMBEGS)

J. W. Wendorf

November 1978 **Communications of the ACM**, Volume 21 Issue 11

Full text available:  [pdf\(898.25 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

SEMBEGS is a complete batch environment graphical system containing components for handling graphical data files, for displaying the contents of these files on a variety of graphical hardware, and for performing graphical batch input operations. SEMBEGS is easy to extend and modify to meet the growing needs of a large batch environment, and is even extendable to a fully interactive system. The paper presents the conceptual view of graphics leading to the design of SEMBEGS and outlines the m ...

**Keywords:** computer graphics, data structures, device independent graphics, graphic display, graphic input, graphical database

5 Binary translation

Richard L. Sites, Anton Chernoff, Matthew B. Kirk, Maurice P. Marks, Scott G. Robinson

February 1993 **Communications of the ACM**, Volume 36 Issue 2

Full text available:  [pdf\(4.84 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**Keywords:** CISC computers, RISC computers, binary translation, computer architecture, processor architecture translation

6 ORACLE a tool for learning compiler writing

William R. Haynes, Charles E. Hughes, Charles P. Pfleeger

February 1977 **ACM SIGCSE Bulletin , Proceedings of the seventh SIGCSE technical symposium on Computer science education**, Volume 9 Issue 1


Full text available:  [pdf\(1.10 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes a compiler called ORACLE which allows a student to examine the actions performed by a simple compiler. Two features are provided to assist the student. The first called replacement mode, provides the necessary conditions to simulate the replacement of three compiler components: symbol table management, lexical analysis, and syntax analysis. Each replacement module is monitored by ORACLE in order to detect errors and to verify correct operation. The second, a trace optio ...

7 Levels of representation of programs and the architecture of universal host machines

B. Ramakrishna Rau

November 1978 **Proceedings of the 11th annual workshop on Microprogramming**

Full text available:  [pdf\(1.12 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The issue of high level language support is treated in a systematic top-down manner. Program representations are categorized into three classes with respect to a host processor: high level representations, directly interpretable representations and directly executable

representations. The space of intermediate languages for high level language support is explored and it is shown that whereas the ideal intermediate language from the point of view of execution time is a directly executable on ...

#### 8 An optimizing compiler for batches of temporal logic formulas

James Ezick

July 2004 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 2004 ACM SIGSOFT international symposium on Software testing and analysis**, Volume 29 Issue 4

Full text available:  [pdf\(282.48 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


Model checking based on validating temporal logic formulas has proven practical and effective for numerous software engineering applications. As systems based on this approach have become more mainstream, a need has arisen to deal effectively with large batches of formulas over a common model. Presently, most systems validate formulas one at a time, with little or no interaction between validation of separate formulas. This is the case despite the fact that, for a wide range of applications, a c ...

**Keywords:** model checking, optimizing compiler, temporal logic

#### 9 The structure of yet another ALGOL compiler

H. Kanner, P. Kosinski, C. L. Robinson

July 1965 **Communications of the ACM**, Volume 8 Issue 7

Full text available:  [pdf\(1.54 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A high-speed "top down" method of syntax analysis which completely eliminates "back-up" of the source string has been implemented in a convenient macro-language. A technique of simulation at compile time of the use of a conventional run-time stack enables the generation of code for expressions which minimizes stores, fetches and stack-pointer motion at run time, while properly treating recursion and side effects of procedures. Block structure and recursion are handle ...

#### 10 Architecture of SOAR: Smalltalk on a RISC

David Ungar, Ricki Blau, Peter Foley, Dain Samples, David Patterson

January 1984 **ACM SIGARCH Computer Architecture News , Proceedings of the 11th annual international symposium on Computer architecture**, Volume 12 Issue 3

Full text available:  [pdf\(1.45 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Smalltalk on a RISC (SOAR) is a simple, Von Neumann computer that is designed to execute the Smalltalk-80 system much faster than existing VLSI microcomputers. The Smalltalk-80 system is a highly productive programming environment but poses tough challenges for implementors: dynamic data typing, a high level instruction set, frequent and expensive procedure calls, and object-oriented storage management. SOAR compiles programs to a low level, efficient instruction set. Parallel tag checks pe ...

#### 11 Machine-independent metacode translation

Thomas Wright

July 1977 **ACM SIGGRAPH Computer Graphics , Proceedings of the 4th annual conference on Computer graphics and interactive techniques**, Volume 11 Issue 2

Full text available:  [pdf\(41.68 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Many systems implement plotter device-independent computer graphics by having a system plot package which outputs a plotter-independent code (here called metacode) and having a translating driver for each plotter which uses this code as input. The translator for this code

can often be run with greatest efficiency on the computer which hosts the plotter. In NCAR's configuration, various computers will drive different plotters, making a portable metacode translator a desirable tool. Constructing a ...

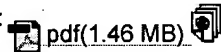
**Keywords:** device-independent graphics, metacode translation, portability

12 Application-controlled demand paging for out-of-core visualization

Michael Cox, David Ellsworth

October 1997 **Proceedings of the 8th conference on Visualization '97**

Full text available:



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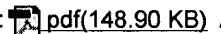
**Keywords:** computational fluid dynamics, out-of-core visualization, visualization

13 The use of sub-routines in programmes

D. J. Wheeler

May 1952 **Proceedings of the 1952 ACM national meeting (Pittsburgh)**

Full text available:



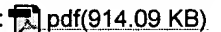
Additional Information: [full citation](#)

14 Man-aided computer translation from English into French using an on-line system to manipulate a bi-lingual conceptual dictionary, or thesaurus

Margaret Masterman

August 1967 **Proceedings of the 1967 conference on Computational linguistics**

Full text available:



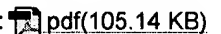
Additional Information: [full citation](#), [references](#)

15 Algorithm 580: QRUP: A Set of FORTRAN Routines for Updating QR Factorizations [F5]

A. Buckley

December 1981 **ACM Transactions on Mathematical Software (TOMS)**, Volume 7 Issue 4

Full text available:



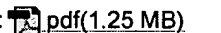
Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

16 Mapping GUIs to auditory interfaces

Elizabeth D. Mynatt, W. Keith Edwards

December 1992 **Proceedings of the 5th annual ACM symposium on User interface software and technology**

Full text available:



Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes work to provide mappings between X-based graphical interfaces and auditory interfaces. In our system, dubbed Mercator, this mapping is transparent to applications. The primary motivation for this work is to provide accessibility to graphical applications for users who are blind or visually impaired. We describe the design of an auditory interface which simulates many of the features of graphical interfaces. We then describe the architecture we have built to model and tr ...

**17 A Fortran 90-based multiprecision system**


David H. Bailey

December 1995 **ACM Transactions on Mathematical Software (TOMS)**, Volume 21 Issue 4Full text available:  pdf(604.66 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

A new version of a Fortran multiprecision computation system, based on the Fortran 90 language, is described. With this new approach, a translator program is not required—translation of Fortran code for multiprecision is accomplished by merely utilizing advanced features of Fortran 90, such as derived data types and operator extensions. This approach results in more-reliable translation and permits programmers of multiprecision applications to utilize the full power of Fortran 90. The ...

**Keywords:** Fortran 90, arithmetic, multiprecision**18 Dynamic restructuring of databases with generation data structures**

Rob Gerritsen, Howard L. Morgan

October 1976 **Proceedings of the annual conference**Full text available:  pdf(602.36 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Most logical database restructuring schemes require a complete pass through the database for reformatting. Our approach is to leave the database in situ and to permit a mixture (several generations) of structures to co-exist. Each generation of structure is described in a Generation Data Structure Schema, which has a generic structure of its own. A Restructuring Data Definition Language is proposed for describing the evolution from one schema to the next. Steps toward implementation are discussed ...

**19 Translation of nested Pascal routines to C**

Neelakantan Sundaresan

May 1990 **ACM SIGPLAN Notices**, Volume 25 Issue 5Full text available:  pdf(798.68 KB) Additional Information: [full citation](#)**20 Graphics Programming Using the Core System**

R. Daniel Bergeron, Peter R. Bono, James D. Foley

December 1978 **ACM Computing Surveys (CSUR)**, Volume 10 Issue 4Full text available:  pdf(2.92 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

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John Colter, Netscape Navigator

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